Electric field control single spins in complex oxides for energy-efficient logic

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Problem in classical computing: Currently cannot reach Landauer limit of switching energy with complementary metal oxide (CMOS) transistors due to tunneling and other short-channel effects (e.g. drain induced barrier lowering). Threshold gate voltage at 0.5 – 1 V. Can we be more energy efficient? Our approach: Spin oxide transistors (SOTs). Ferroelectric oxide material doped with magnetic ions. Electrostatically gated oxide layer induces polarization and drives magnetization of single dopant spin, via spin-orbit coupling. Threshold voltage ~ 150 mV. Ease of field control of single spin determined from magnetocrystalline anisotropy energy (MCAE).



Electrostatically gate doped oxide-layer.







Electric stimulus induces ferroelectric distortion determined by crystal field.

Polarization couples to dopant magnetic moment via spin-orbit, controlled by MCAE.

Magnetization response readout corresponding to logical '0' or '1'.

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(1) Select trial host oxide and dopant.	(2) Compute MCAE over grid.
Host material candidates selected based on screening criteria: - Ferroelectric ordering - Multi-step polarariz. switching pathway - No magnetic ordering	SOK (Spin Orbit Kit) $\hat{H} = \hat{H}_{DFT+U} + \lambda \hat{L} \cdot \hat{S}$ $E_{tot} = \sum_{i} \epsilon(\theta_s, \phi_s)$ Provided the relaxed supercell from (1), MCAE over a defined
Dopant candidates selected based on criteria: - Non-zero net spin (e.g. Mn, Fe, Co) - Distorted local crystal field environment - Strong spin-orbit coupling with local environment	spin mesh is determined via SOK . SOK is an opensource code we developed for prediction/analysis of MCAE surfaces. Interfaces with standard DFT codes like VASP.
(3) Determine easy axes & planes. SOCK outputs MCAE for provided 'spin' transistor. Orientation corresponding to minimum in MCAE correspond to 'easy axis'. MCAE surface fitted to phenomenological expression and MCAE constants are extracted. $U_{MCA} = K^{(0)} + K^{(1)}m_i + K^{(2)}m_im_i$	(4) Find mechanism. Grow & measure. $\int d_{A} = \int d_{A}$

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Conclusion and next steps

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$$U_{MCAE}(\theta_{s},\phi_{s}) = \sum_{m_{l},m_{l}'} \sum_{m_{s},m_{s}'} \epsilon_{m_{l},m_{s}}^{CF} + \frac{|M_{m_{l},m_{s},m_{s}'}(\theta_{s},\phi_{s})|^{2}}{\epsilon_{m_{l}',m_{s}'}^{CF} - \epsilon_{m_{l},m_{s}}^{CF}}$$

MCAE for spin-oxide transistor depends on crystal field of the dopant atom and degree of spin-orbit coupling between dopant spin moment and its surrounding orbitals. Degree of anisotropy in the crystal field will influence the shape of the MCAE surface. Shape of engineered MCAE surface determines the how the spin transistor switches between its logical '0' and '1' state and its associated switching energy (threshold voltage). Our goal is to explore new candidate host materials and dopants that minimize the switching energy and to identify the dominant anisotropy mechanism.

